	Application No.	Applicant(s)
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Notice of Allowability	10/629,526	KADOTANI, TSUTOMU Art Unit
nodes of Anovasiney	Examiner	Artonit
	Toan Ton	2871
The MAILING DATE of this communication appearance All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this a or other appropriate communication IGHTS. This application is subject	application. If not included on will be mailed in due course. THIS
1. A This communication is responsive to papers filed 02/11/05	į.	
2. The allowed claim(s) is/are <u>1-12</u> .		
3. 🖾 The drawings filed on 29 July 2003 are accepted by the Examiner.		
 4. Acknowledgment is made of a claim for foreign priority una) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	e been received. e been received in Application No.	
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		ly complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give		
6. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.	
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached		
1) hereto or 2) to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on the draw the header according to 37 CFR 1.12	vings in the front (not the back) of 1(d).
7. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT	sit of BIOLOGICAL MATERIAL FOR THE DEPOSIT OF BIOLOGI	. must be submitted. Note the CAL MATERIAL.
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview Summar	
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	Paper No./Mail D 08), 7. ⊠ Examiner's Amen	dment/Comment
4. Examiner's Comment Regarding Requirement for Deposit of Biological Material A. Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ⊠ Examiner's Stater 9. □ Other	ment of Reasons for Allowance

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Art Unit: 2871

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows: nonelected claim 13 (without traverse) has been canceled.

REASONS FOR ALLOWANCE

2. Claims 1-12 are allowed.

The following is an examiner's statement of reasons for allowance: the prior art of record does not anticipate nor render obvious to one ordinary skilled in the art a liquid crystal display device comprising a combination of various elements as claimed, more specifically, a black matrix mounted on either of the opposing panels for defining an array of pixels each having a pixel area in the LCD device, an interconnect layer mounted on one of the opposing panels and patterned to a plurality of interconnect lines formed on a common insulator, wherein one of the interconnect lines extending in the pixel area is lower in a product of a thickness thereof by a surface reflectance thereof than another of the interconnect lines extending outside the pixel area.

Matsumoto ('699) discloses an IPS LCD device comprising a plurality of data lines, a plurality gate lines, a pixel electrode, an insulator, a common electrode interconnect line comprising ITO. However, Matsumoto fails to disclose 'a black matrix mounted on either of the opposing panels for defining an array of pixels each having a pixel area in the LCD device, an interconnect layer mounted on one of the opposing panels and patterned to a plurality of interconnect lines formed on a common insulator, wherein one of the interconnect lines extending in the pixel area is lower in a product of a thickness thereof by a surface reflectance thereof than another of the interconnect lines extending outside the pixel area'.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for

Allowance."

CONTACT INFORMATION

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Toan Ton whose telephone number is (571) 272-2303.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

April 27, 2005

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